

LC420W02
Liquid Crystal Display

Product Specification

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(●) Final Specification

Title	42.0" WXGA TFT LCD
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BUYER	
MODEL	-

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC420W02
MODEL	A4K3

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY

SIGNATURE
DATE

/

/

/

Please return 1 copy for your confirmation with
your signature and comments.

APPROVED BY

SIGNATURE
DATE

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LG. Philips LCD Co., Ltd



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RECORD OF REVISIONS

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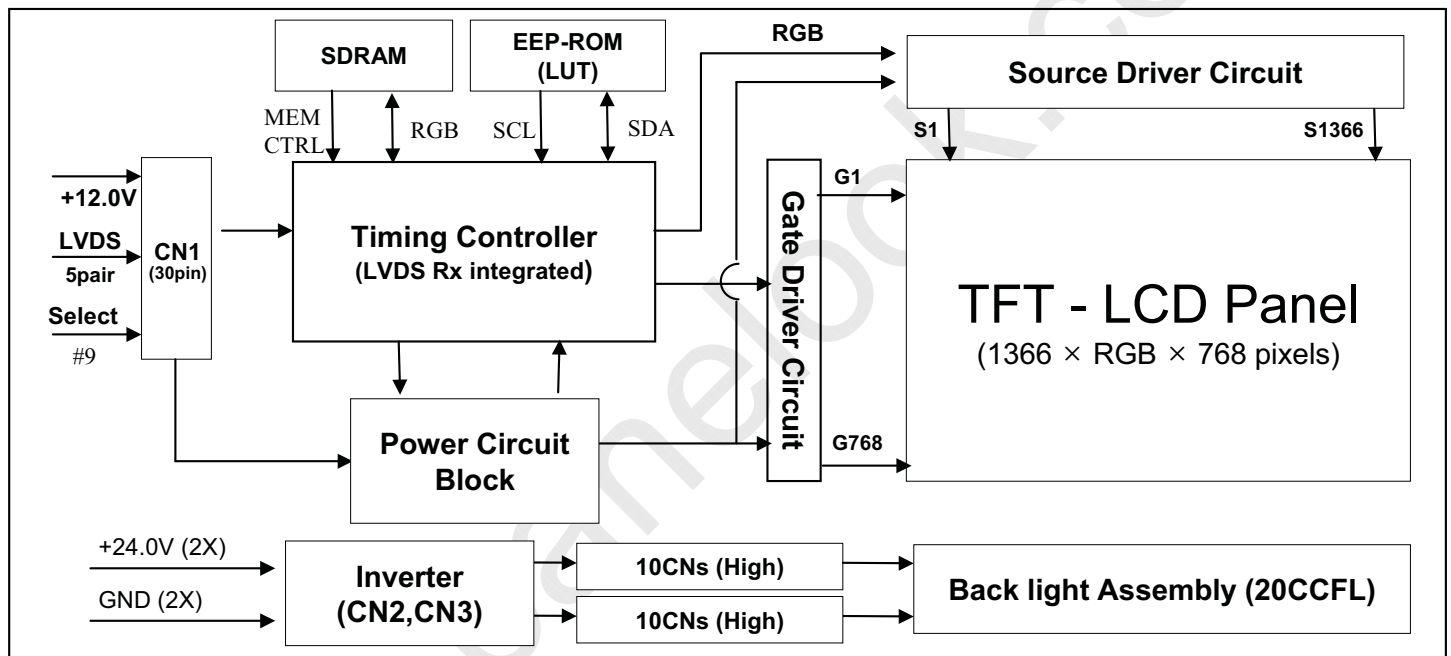
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1. General Description

LC420W02 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 42.0 inch diagonally measured active display area with WXGA resolution (768 vertical by 1366 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16,7M(True) colors.

It has been designed to apply the 8-bit 1 port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	42.02 inches(1067.308mm) diagonal
Outline Dimension	1006 mm(H) x 610 mm(V) x 56 mm(D) (Typ.)
Pixel Pitch	0.227mm x 0.681mm x RGB
Pixel Format	1366 horiz. By 768 vert. Pixels RGB strip arrangement
Color Depth	8-bit, 16.7 M colors
Luminance, White	500 cd/m ² (Center 1-point) (Typ.)
Viewing Angle (CR>10)	Viewing Angle Free (R/L 176 (Typ.), U/D 176(Typ))
Power Consumption	Total 168.3W (Typ.) (Logic=6.5W, Lamp=161.8W [I_{BL} =6.5mA])
Weight	11,800g (Typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer

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2. Absolute Maximum Ratings

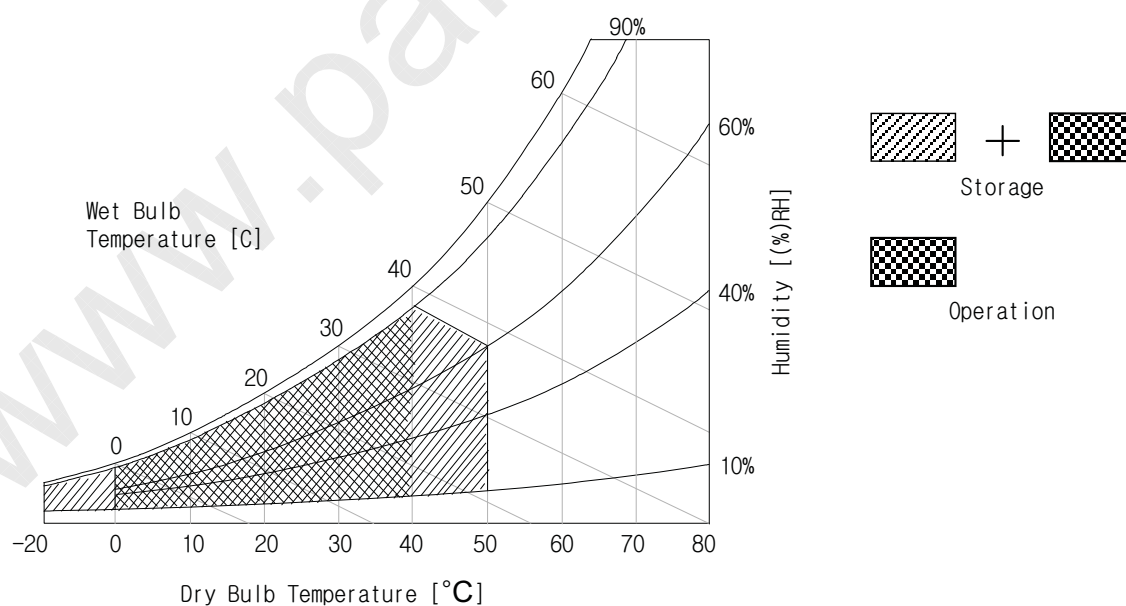
The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Values		Units	Notes
			Min	Max		
Power Input Voltage	LCM	VLCD	-0.3	+14.0	Vdc	at 25 ± 2 °C
	Backlight inverter	VLB	+21.6	+28	Vdc	
ON/OFF Control Voltage		VON/OFF	-0.3	+5.25	Vdc	
Brightness Control Voltage		VBr	0	+3.3	Vdc	
Operating Temperature		T _{OP}	0	+40	°C	1
Storage Temperature		T _{ST}	-20	+50	°C	
Operating Ambient Humidity		H _{OP}	10	90	%RH	
Storage Humidity		H _{ST}	10	90	%RH	

Note : 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the CCFL/Backlight, is to power inverter.

Table 2_1. ELECTRICAL CHARACTERISTICS

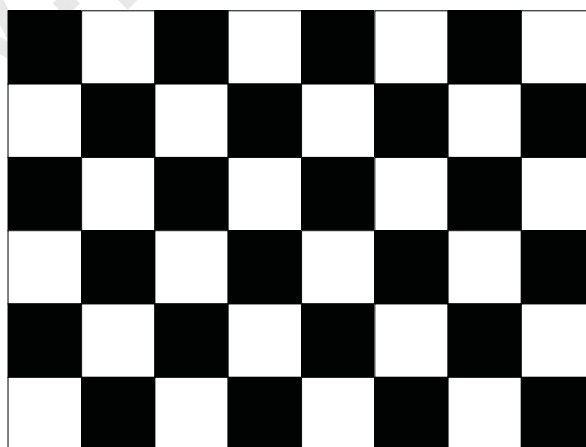
Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
MODULE :						
Power Supply Input Voltage	VLCD	11.4	12.0	12.6	Vdc	
Power Supply Input Current	ILCD	-	545	635	mA	1
		-	700	805	mA	2
Power Consumption	PLCD		6.5	9.7	Watt	1
Rush current	IRUSH	-	-	3.0	A	3

Note :

1. The specified current and power consumption are under the $V_{LCD}=12.0V$, $25 \pm 2^{\circ}C$, $f_v=60Hz$ condition whereas mosaic pattern(8 x 6) is displayed and f_v is the frame frequency.
2. The current is specified at the maximum current pattern.
3. The duration of rush current is about 2ms and rising time of power Input is 1ms(min.).

White : 255Gray

Black : 0Gray



Mosaic Pattern(8 x 6)



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Table 2_2. ELECTRICAL CHARACTERISTICS

Parameter			Symbol	Values			Unit	Notes
				Min	Typ	Max		
Inverter :								
Power Supply Input Voltage			VBL	22.8	24.0	26.2	Vdc	1
Power Supply Input Voltage Ripple				-0.2		0.2	Vp-p	
Unloading Input Voltage						28.0	Vdc	
Power Supply Input Current			IBL	-	6.74	8.11	A	1
Power Consumption			PBL	-	161.8	194.6	W	1
Input Voltage for Control System Signals	Brightness Adjust		VBR	0.0		3.3	Vdc	2
	On/Off	On	V on	2.5	-	5.0	Vdc	
		Off	V off	-0.3	0.0	0.5	Vdc	
Lamp :								
Life Time				50,000			Hrs	3

Notes :

1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120 minutes at $25 \pm 2^\circ\text{C}$

The specified current and power consumption are under the typical supply Input voltage, it is total power consumption.

Ripple voltage of the Power Supply Input Voltage is under 0.2 Vp-p.

LPL recommend Input Voltage is $24.0\text{V} \pm 5\%$.

(Philips system should keep the input voltage as $25.5\text{V} \pm 2\%$)

2. Brightness Control

This VBR Voltage control brightness.

VBR Voltage	Function
3.3V	Maximum Brightness (100%)
0V	Minimum Brightness.(15~25%)

3. The Life Time is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at $25 \pm 2^\circ\text{C}$.

Specified value is when lamp is aligned horizontally.



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3-2. Interface Connections

This LCD employs three interface connections, a 30-pin connector is used for the module electronics and a 12-pin Connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1) : FI-X30SSL-HF (Manufactured by JAE) or Equivalent
- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No	Symbol	Description	Output Pin # (LVDS Tx)
1	VLCD	Power Supply +12.0V	Note 1
2	VLCD	Power Supply +12.0V	
3	VLCD	Power Supply +12.0V	
4	VLCD	Power Supply +12.0V	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	Select	Select LVDS Data format	
10	NC	NC	
11	GND	Ground	
12	RA-	LVDS Receiver Signal(-)	
13	RA+	LVDS Receiver Signal(+)	
14	GND	Ground	
15	RB-	LVDS Receiver Signal(-)	
16	RB+	LVDS Receiver Signal(+)	
17	GND	Ground	
18	RC-	LVDS Receiver Signal(-)	
19	RC+	LVDS Receiver Signal(+)	
20	GND	Ground	
21	RCLK-	LVDS Receiver Clock Signal(-)	
22	RCLK+	LVDS Receiver Clock Signal(+)	
23	GND	Ground	
24	RD-	LVDS Receiver Signal(-)	
25	RD+	LVDS Receiver Signal(+)	
26	GND	Ground	
27	NC	NC	
28	NC	NC	
29	GND	Ground	
30	NC(GND)	LCD test	Note 2

Note: 1. If Pin9 is Ground, interface format is "LG", and if Pin9 is Vcc(3.3V), interface format is "DISM"
See page 9 and 10.

2. Pin30 is necessary for LCD test.

When LVDS signals are abnormal operation more than 3-Vsync times and power 12V is supplied,

'Open' or 'Vcc' : LCD operate itself some test patterns.(AGP – Auto Generation Pattern)

'Ground' : LCD operate itself a black pattern. (NSB – No Signal Black)

LPL recommends 'Ground' for NSB.

3. All GND(ground) pins should be connected together, which should be also connected to the LCD module metal frame.

4. All VLCD (power input) pins should be connected together.

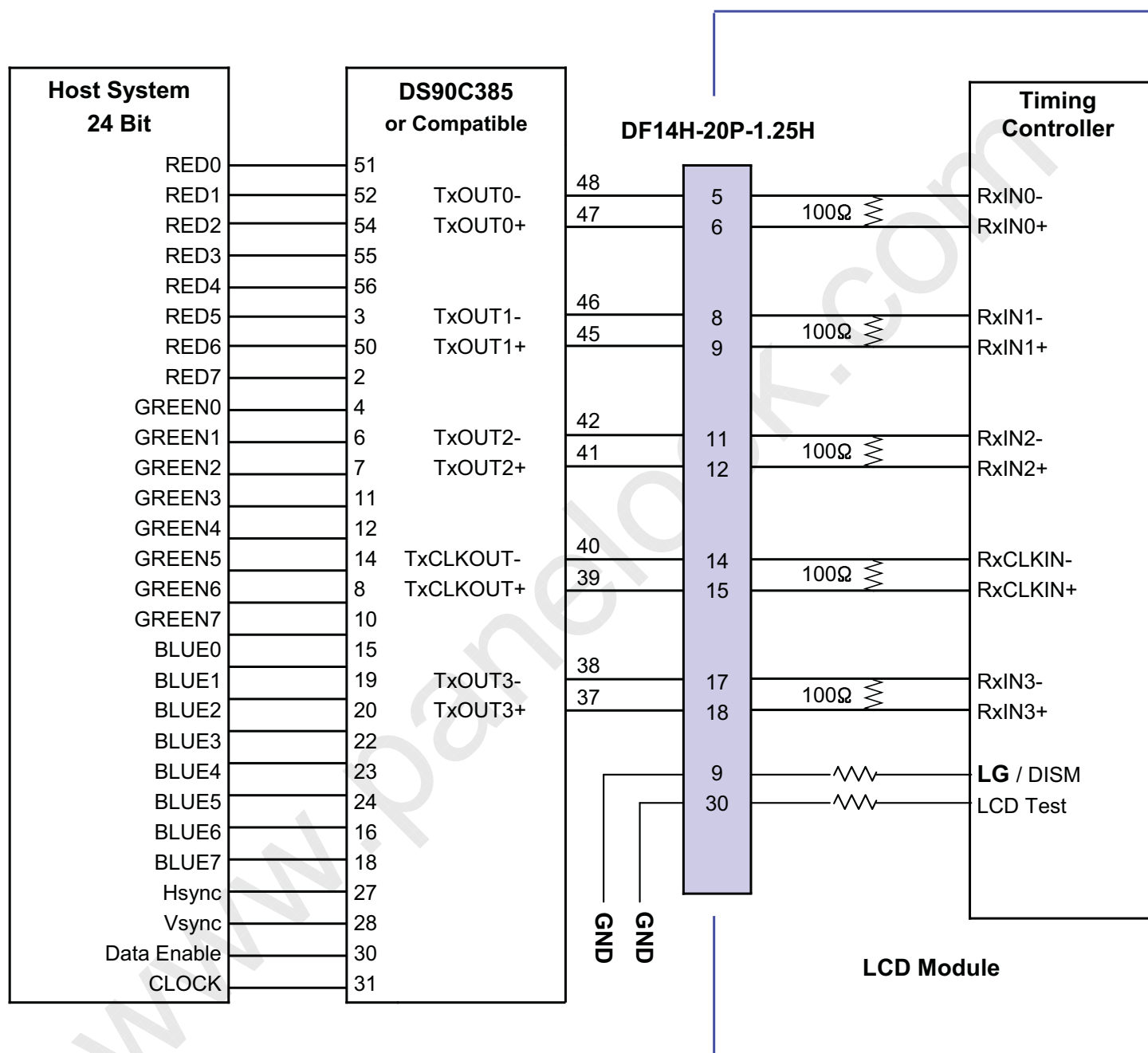
5. Input Levels of LVDS signals are based on the IEA 664 Standard.



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Table 4. REQUIRED SIGNAL ASSIGNMENT FOR LVDS TRANSMITTER (Pin9="L" or "Open")



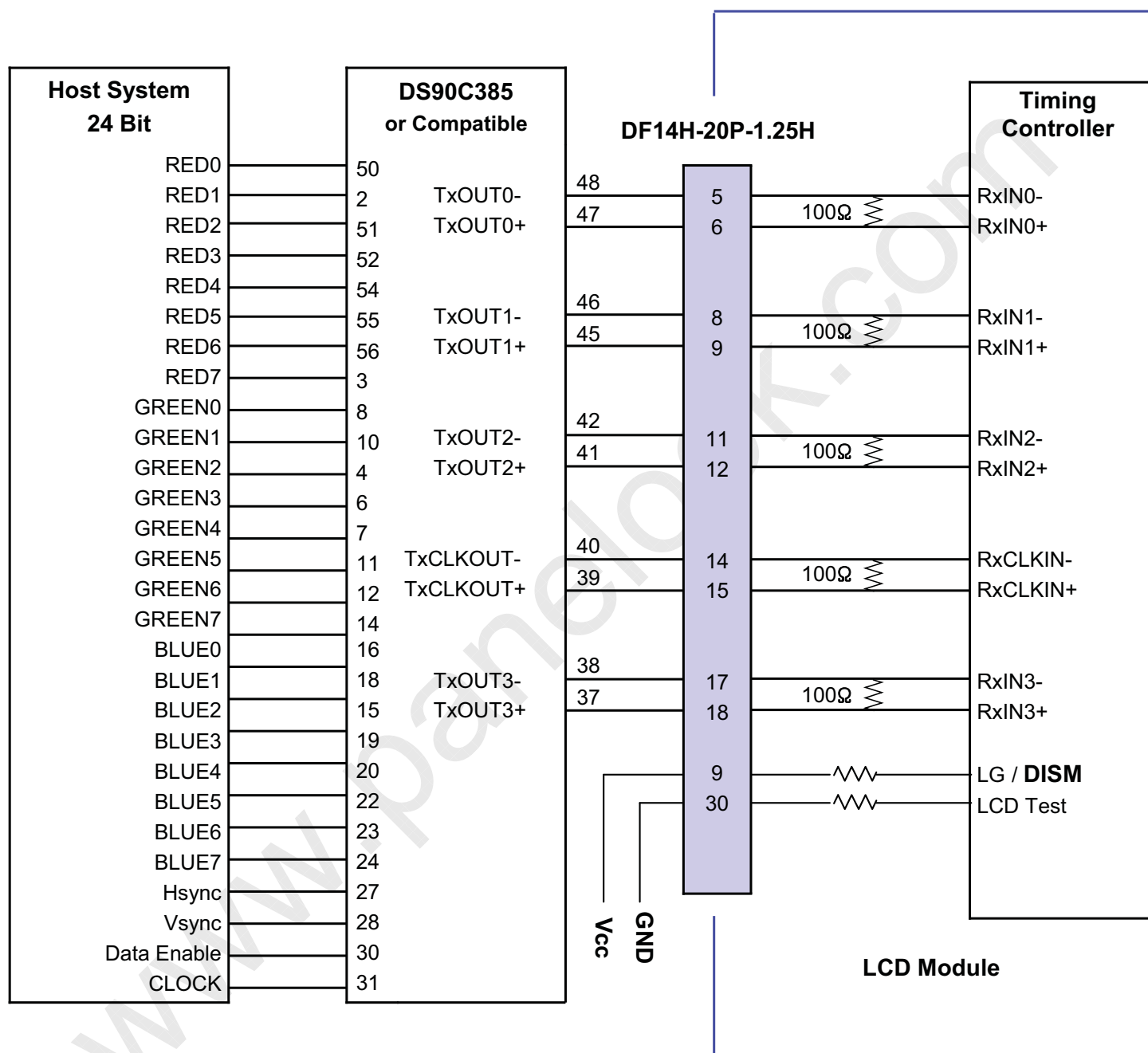
- Note: 1. The LCD Module uses a 100 Ohm resistor between positive and negative lines of each receiver input.
 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (DS90C385 or Compatible)
 3. '7' means MSB and '0' means LSB at R,G,B pixel data.



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Table 5. REQUIRED SIGNAL ASSIGNMENT FOR LVDS TRANSMITTER (Pin9="H")



- Note: 1. The LCD Module uses a 100 Ohm resistor between positive and negative lines of each receiver input.
 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (DS90C385 or Compatible)
 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

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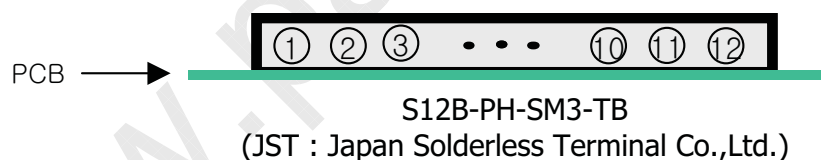
3-2-2. Backlight Inverter

- Inverter Connector : S12B-PH-SM3(manufactured by JST) or Equivalent
- Mating Connector : PHR-12 or Equivalent

Table 6. INVERTER CONNECTOR PIN CONFIGURATION

Pin No	Symbol	Description	Remarks
1	VBL	Power Supply + 24V	
2	VBL	Power Supply + 24V	
3	VBL	Power Supply + 24V	
4	VBL	Power Supply + 24V	
5	VBL	Power Supply + 24V	
6	GND	POWER GND	Note 1
7	GND	POWER GND	
8	GND	POWER GND	
9	GND	POWER GND	
10	GND	POWER GND	
11	VBR	0V ~ 3.3V	Note 2
12	On/Off	0V ~ 5.0V	Note 3

Notes : 1. GND is connected to the LCD module metal frame.



2. 0V : Minimum Brightness.
3.3V : Maximum Brightness.
3. On : 2.5~5.0V
Off : -0.3~0.5V



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3-3. Signal Timing Specifications

This is the signal timing required at the input of LVDS Transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 7. Timing Table

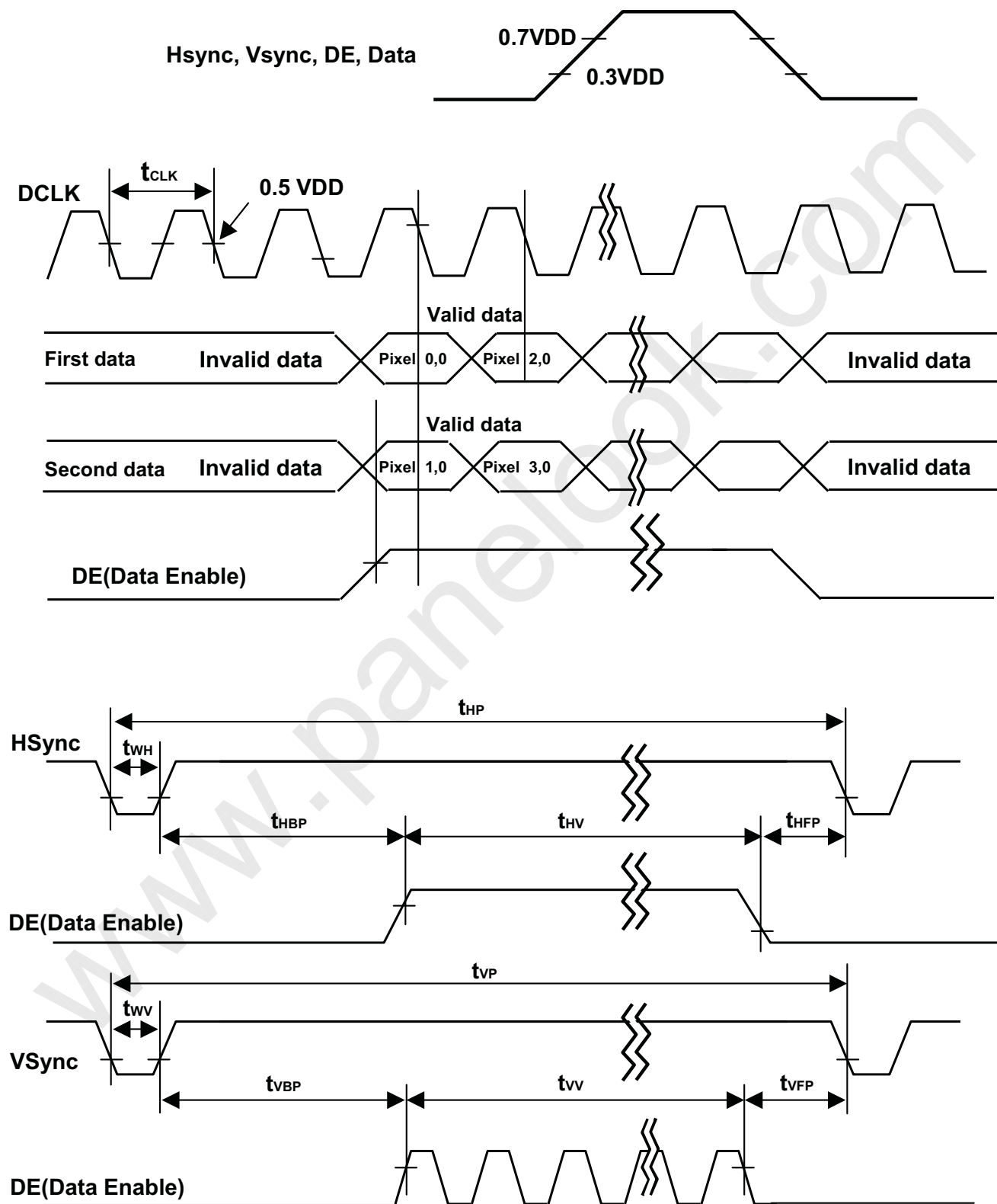
ITEM	Symbol		Min	Typ	Max	Unit	Note
DCLK	Period	tCLK	12.2	13.8	14.7	ns	
	Frequency	-	68	72	80	MHz	
Hsync	Period	tHP	1414	1526	1777	tCLK	
	Frequency	fH	45	47.4	50	KHz	
	Width	tWH	8	32	-	tCLK	
Vsync	Period	tVP	775	790	1063	tHP	Note 1) PAL : 47~53Hz NTSC : 57~63Hz
	Frequency	fV	47	60	63	Hz	
	Width	tWV	2	5	-	tHP	
DE (Data Enable)	Horizontal Valid	tHV	1366	1366	1366	tCLK	
	Horizontal Back Porch	tHBP	16	80	-		
	Horizontal Front Porch	tHFP	24	48	-		
	Horizontal Blank	-	48	160	tHP- tHV		
	Vertical Valid	tV	768	768	768	tHP	
	Vertical Back Porch	tVBP	4	15	-		
	Vertical Front Porch	tVFP	1	2	-		
	Vertical Blank	-	7	22	tVP- tV		

Note: Hsync Period and Hsync Width should be even number times of tCLK. If the value is odd number times of tCLK, display control signal can be asynchronous. In order to operate this LCD module, Hsync, Vsync and DE(data enable) signals should be used.

1. : The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.
2. Vsync and Hsync should be keep the above specification.
3. Hsync Period, Hsync Width and Horizontal Back Porch should be any times of character number(8).

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3-4. Signal Timing Waveforms





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3-5. Color Data Reference

The Brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 8. COLOR DATA REFERENCE

Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB				LSB				MSB				LSB				MSB				LSB			
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...	...								...								...							
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	...	...								...								...							
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	...	...								...								...							
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note : Users should be input true 8 Bit data streams via LVDS transmitter.

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3-6. Power Sequence

3-6-1. LCD Driving circuit

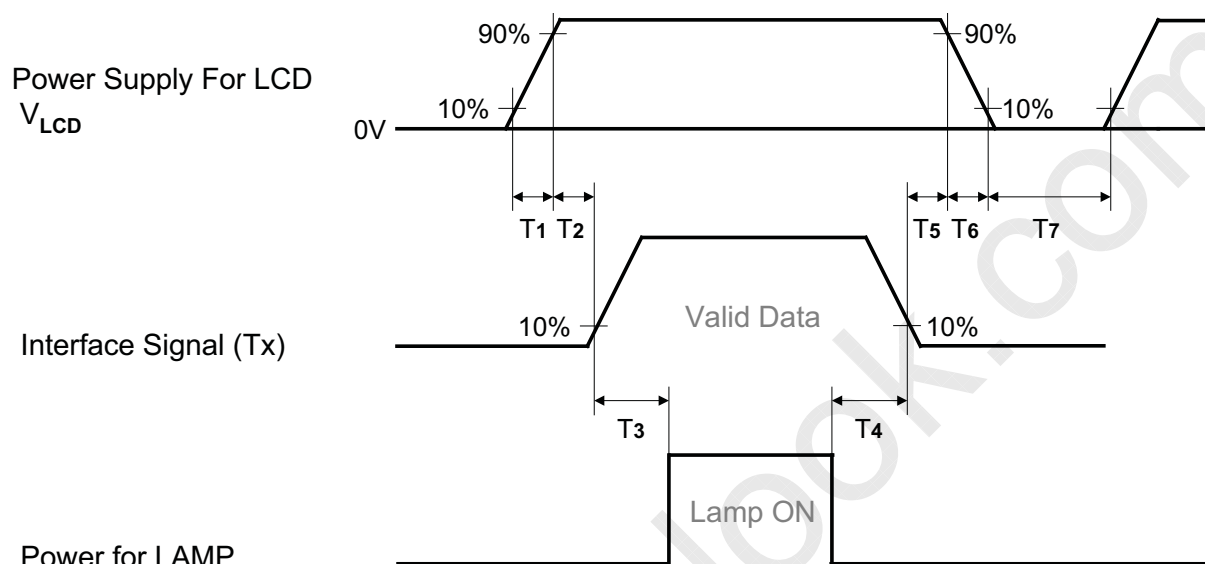


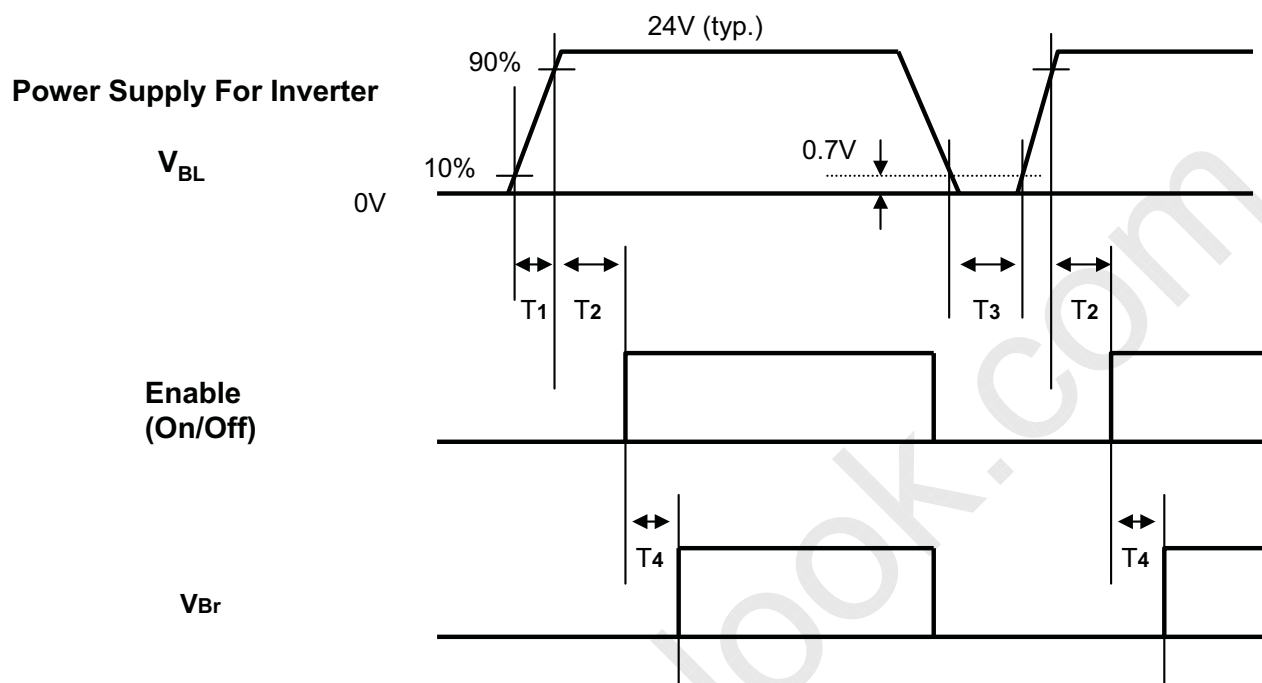
Table 9-1. Power Sequence for LCD Driving circuit

Parameter	Values			Units
	Min	Typ	Max	
T1	0.5	-	10	ms
T2	0.5	-	50	ms
T3	200	-	-	ms
T4	200	-	-	ms
T5	0.5	-	50	ms
T6	-	-	100	ms
T7	1	-	-	s

- Notes :
1. Please avoid floating state of interface signal at invalid period.
 2. When the interface signal is invalid, be sure to pull down the power supply V_{LCD} to 0V.
 3. Lamp power must be turn on after interface signals are valid.

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3-6-2. On/Off for Inverter



3-6-3. Deep condition for Inverter

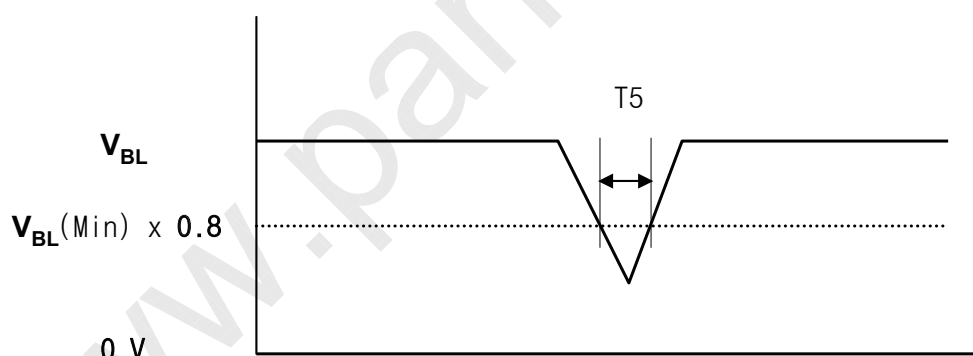


Table 9-2. Power Sequence for Inverter

Parameter	Values			Units	Remarks
	Min	Typ	Max		
T1	20	-	-	ms	inverters connected condition
T2	500	-	-	ms	
T3	200	-	-	ms	
T4	0	-	-	ms	
T5	-	-	10	ms	$V_{BL(Min)} \times 0.8$

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30Min in a dark environment at $25\pm 2^{\circ}\text{C}$. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° .

FIG. 1 Presents additional information concerning the measurement equipment and method.

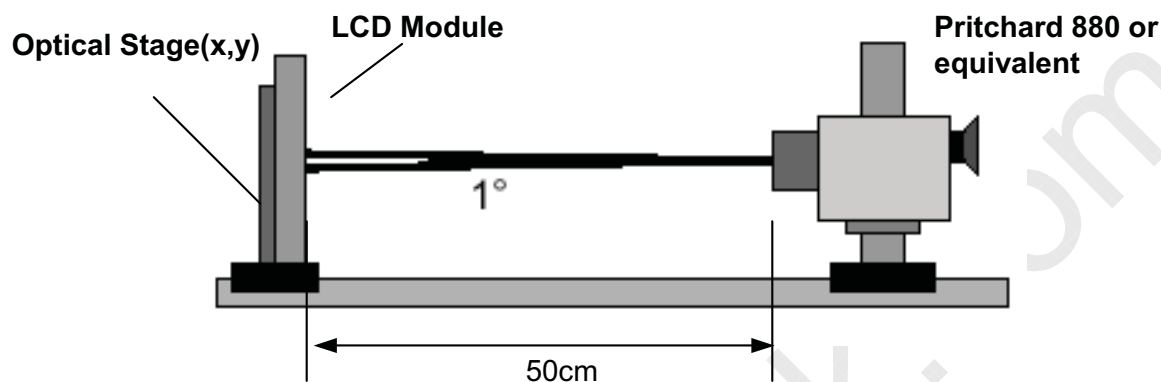


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

$T_a = 25\pm 2^{\circ}\text{C}$, $V_{\text{LCD}} = 12.0\text{V}$, $f_v = 60\text{Hz}$, $D_{\text{clk}} = 72\text{MHz}$

Parameter		Symbol	Values			Units	Notes
			Min	Typ	Max		
Contrast Ratio		CR	300	400			1
Surface Luminance, white		L_{WH}	400	500		cd/m^2	2
Luminance Variation		δ_{WHITE} 5P			1.3		3
Response Time (Gray-to-Gray)	Rise Time	Tr_R	-	10	16	ms	4
	Decay Time	Tr_D	-	10	16	ms	4
Color Coordinates [CIE1931]	RED	R_x	Typ -0.03	0.639	Typ +0.03		
		R_y		0.342			
	GREEN	G_x		0.282			
		G_y		0.610			
	BLUE	B_x		0.147			
		B_y		0.073			
	WHITE	W_x		0.281			
		W_y		0.293			
Viewing Angle (CR>10)							
	x axis, right($\phi=0^{\circ}$)	θ_r	85	88	-	degree	5
	x axis, left ($\phi=180^{\circ}$)	θ_l	85	88	-		
	y axis, up ($\phi=90^{\circ}$)	θ_u	85	88	-		
	y axis, down ($\phi=270^{\circ}$)	θ_d	85	88	-		
Gray Scale							6



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Notes 1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

It is measured at center 1-point

- Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 2.
- The variation in surface luminance, δ WHITE is defined as :

$$\delta \text{ WHITE}(5P) = \text{Maximum}(L_{on1}, L_{on2}, L_{on3}, \dots, L_{on5}) / \text{Minimum}(L_{on1}, L_{on2}, L_{on3}, \dots, L_{on5})$$
 Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations .
 For more information, see FIG 2.
- Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see FIG 3. ($N < M$)
- Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface.
 For more information, see FIG 4.
- Gray scale specification
 Gamma Value is approximately 2.2.
 For more information, see Table 11.

Table 11. Gray Scale Specification

Gray Level	Luminance [%] (Typ)
L0	0.23
L15	0.36
L31	1.09
L47	2.28
L63	4.58
L79	7.15
L95	10.82
L111	15.72
L127	21.93
L143	27.81
L159	34.94
L175	43.32
L191	53.12
L207	63.10
L223	74.51
L239	86.99
L255	100.00

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Measuring point for surface luminance & measuring point for luminance variation

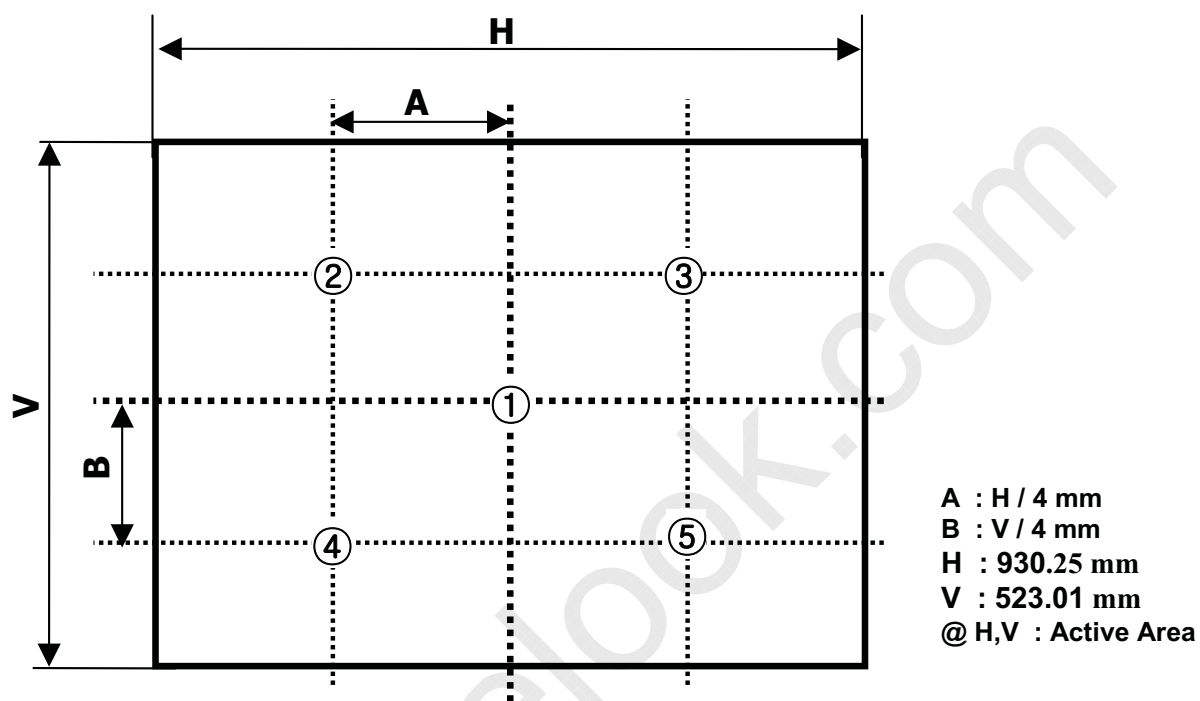


FIG. 2 Measure Point for Luminance

The response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

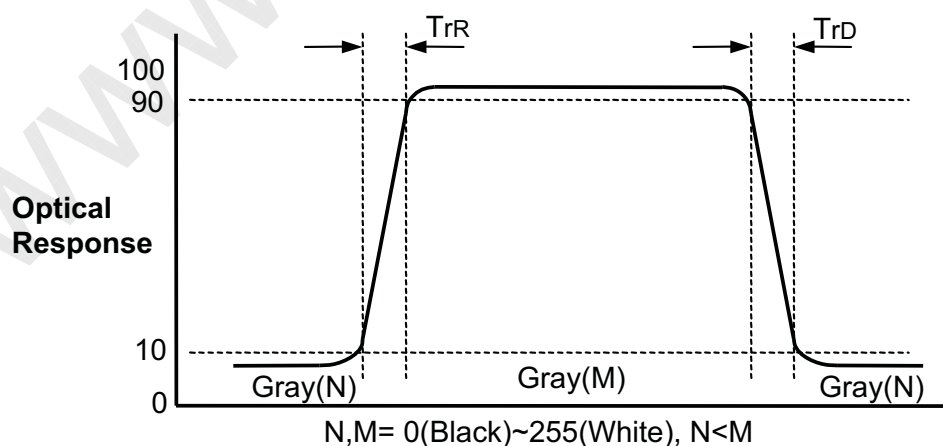


FIG. 3 Response Time

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Dimension of viewing angle range

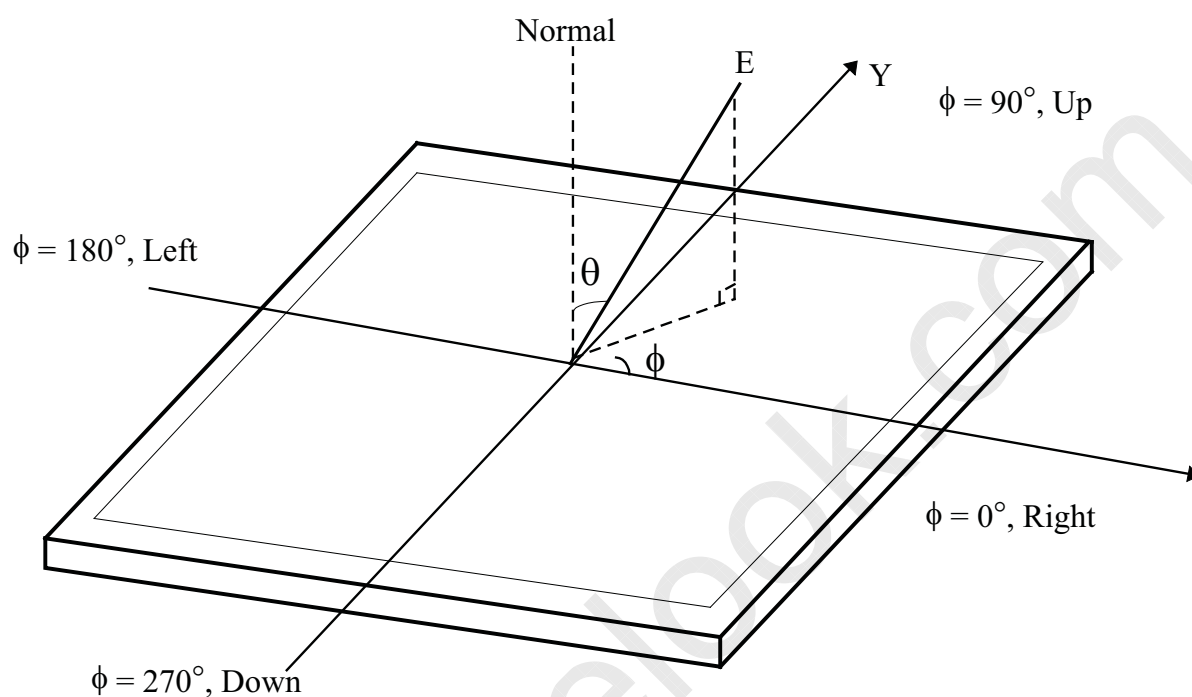


FIG. 4 Viewing angle

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5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal	1006.0 mm
	Vertical	610.0 mm
	Depth	56.0 mm
Bezel Area	Horizontal	939mm
	Vertical	531mm
Active Display Area	Horizontal	930.25mm
	Vertical	523.01mm
Weight	11,800g (Typ.)	
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer	

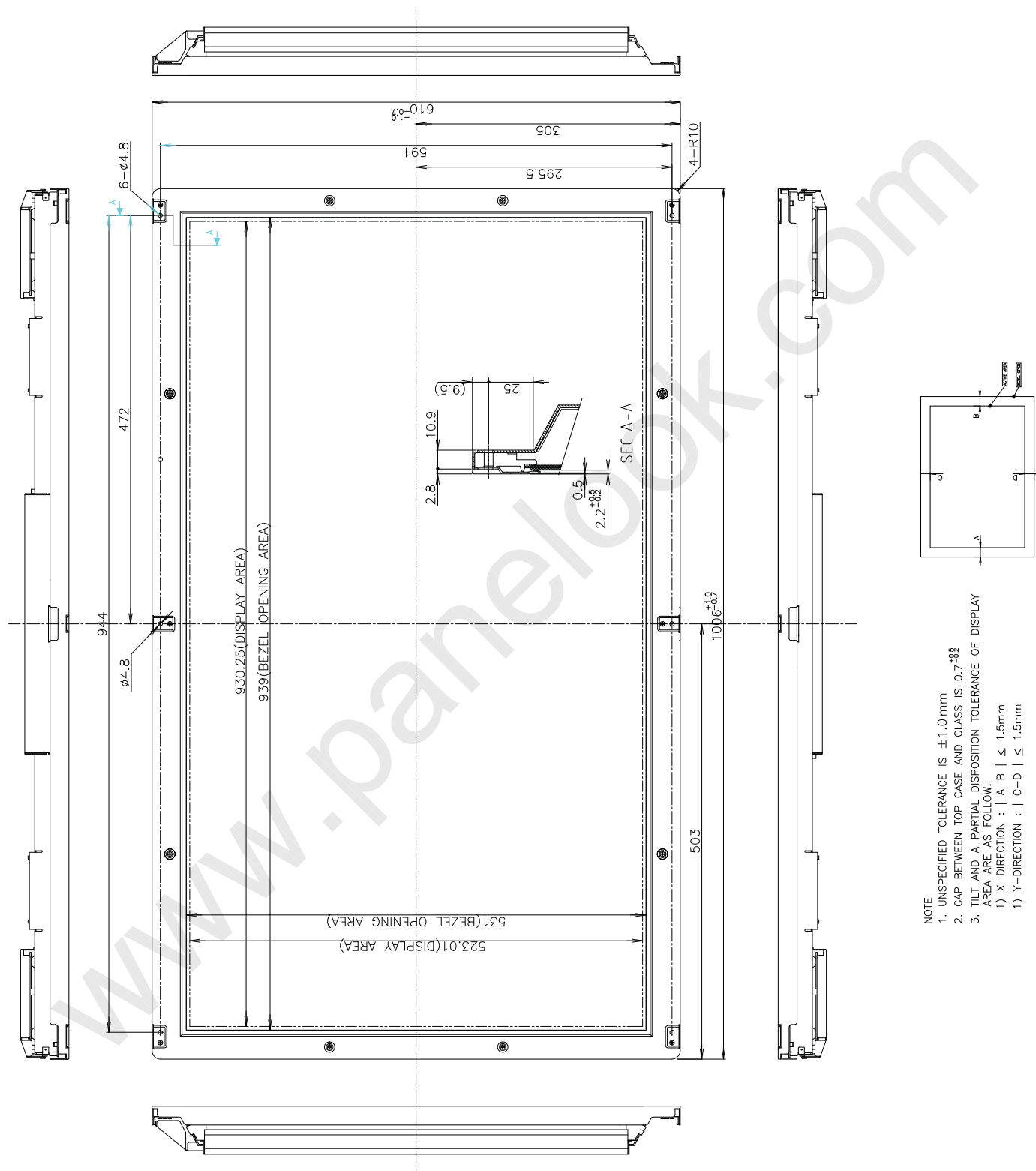
Notes : Please refer to a mechanic drawing in terms of tolerance at the next page.



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<FRONT VIEW>



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6. Reliability

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta= 50°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 40°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-500Hz Duration : X,Y,Z, 10 min One time each direction
6	Shock test (non-operating)	Shock level : 100Grms Waveform : half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	0 - 14,000 feet(4267.2m) 0 - 40,000 feet(12192m)



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7. International standards

7-1. Safety

- a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000.
Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
- b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000.
Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
- c) EN 60950 : 2000, Third Edition
IEC 60950 : 1999, Third Edition
European Committee for Electrotechnical Standardization(CENELEC)
EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998
(Including A1: 2000)

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH)

D : YEAR

F : PANEL CODE

H : ASSEMBLY CODE

E : MONTH

G : FACTORY CODE

I,J,K,L,M : SERIAL NO.

Note

1. YEAR

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	A	B	C

3. PANEL CODE

Panel Code	P1 Factory	P2 Factory	P3 Factory	P4 Factory	P5 Factory	Hydis Panel
Mark	1	2	3	4	5	H

4. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing
Mark	K	C

5. SERIAL NO.

Year	1 ~ 99999	100000 ~
Mark	00001 ~ 99999	A0001 ~ A9999, , Z9999

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 3 pcs

b) Box Size : 1144mm X 462mm X 715mm



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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
(if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.



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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape.
When the protection film is peeled off, static electricity is generated between the film and polarizer.
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.